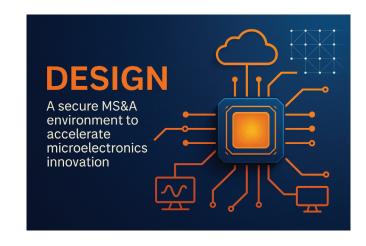# DESIGN
PLATFORM

DIGITAL ELECTRONICS SYSTEM INTEGRATED GATEWAY NETWORK

OVERVIEW:

DESIGN is an advanced hybrid-cloud MS&A environment that unites design tools, IP resources, and collaborative workflows to accelerate microelectronics innovation from concept to fabrication by lowering the barriers to participation. It enables members to create, simulate, and validate designs across multiple technology domains including analog, digital, RF, and mixed-signal systems within a secure, scalable, and cloud-enabled environment.

The platform integrates EDA tools, process design kits (PDKs), and verified IP blocks to support end-to-end design cycles. DESIGN enhances productivity through version control and data traceability while providing seamless connectivity to MOSIS 2.0 for rapid prototype fabrication. Design enablement and model generation reduce iteration time, allowing members to innovate faster and more efficiently.

DESIGN also supports cross-node collaboration and benchmarking, enabling distributed teams to co-develop and share reusable IP and workflows, fostering interoperability and standardization across the MMEC ecosystem.

DESIGN
A secure MS&A environment to accelerate microelectronics innovation

FEATURES:

• Comprehensive EDA and PDK Integration

• Verified IP Library

• Coordination with CONNECT Platform

• End-to-End Design-to-Fab Workflow with MOSIS 2.0

• Secure Cloud Collaboration and Version Control

• Data Traceability, Governance, and Ontology

DESIGN ECOSYSTEM:

USERS → MARKET PLACE → DESIGN → CLOUD Gov & Commercial (EDA TOOLS, DIGITAL TWIN, SECURITY, HPC & EMULATION, LAB TO FAB PDKs) → RESULTS (DESIGN AUTOMATION, ENHANCED VERIFICATION, OBSOLESCENCE, COST REDUCTION)