

One Platform, Every Tool You Need

The DESIGN Platform provides an integrated semiconductor development infrastructure connecting design tools, IP ecosystems, technology access, & pathways to fabrication within a unified environment, cloud or on-premise.

Purpose-built for startups, small businesses, & design teams who need production-grade EDA tooling, foundry-validated flows, & a single managed workspace without the overhead of managing multiple vendor relationships independently.

PLATFORM CAPABILITIES

Unified Multi-Vendor Workspace

Run Cadence, Keysight, Siemens, Synopsys, & more within a single managed environment. Combine best-of-breed tools across synthesis, P&R, DRC/LVS, & simulation.

Foundry-Validated Design Flows

End-to-end flows vetted against PDK & DRM requirements, from RTL through signoff & GDSII handoff. Reduce tapeout risk with flows tested against foundry acceptance criteria.

Cloud & On-Premise Deployment

Rapid start whether you deploy in the cloud or on-premise: same platform, same flows. Move bespoke configurations seamlessly between environments to flex compute resources to match workloads.

MeTRA®-Powered Secure Collaboration

Integrated MeTRA® knowledge management enables secure file sharing, design data exchange, & team collaboration. Capture, track, & analyze design artifacts across distributed teams with full traceability.

Integrated EDA Version Control COMING SOON

Design versioning tracks every iteration from RTL through layout. Branch, merge, & tag design milestones to enable reproducible tapeouts & confident design reuse across projects.

Automations COMING SOON

Scriptable workflow automations to streamline repetitive design tasks, from batch DRC runs & regression testing to automated signoff checklists & report generation.

DESIGN PLATFORM

WHY DESIGN PLATFORM?

Tapeout Proven Flows

Reduce iterations & de-risk first silicon for teams new to physical design

Full Stack, Single Platform

Synthesis, simulation, DRC/LVS, & GDSII export - all under one roof

Flexible Licensing

SaaS model scales from single-seat prototype to multi-engineer production

VALIDATED FLOW ROADMAP



GlobalFoundries GF130G SUMMER 2026

130nm general-purpose CMOS for mixed-signal, embedded, & IoT designs



GlobalFoundries GF22FDX COMING SOON

22nm FD-SOI for low-power, high-performance RF & mixed-signal design



Onsemi ONK65 COMING SOON

65nm CMOS for low-power, mixed-signal & RF ASIC designs

MMEC's FAMILY OF PRODUCTS

CONNECT PLATFORM

Industry collaboration network connecting design teams, foundries, & IP providers across the microelectronics ecosystem.

DESIGN PLATFORM

Production EDA environment with validated flows loaded like records from a jukebox, so you can switch technologies seamlessly as your program evolves.

SECURE SERVICES

Managed security services, expert training, & guidance to help organizations meet government security, cybersecurity, export, & audit requirements.

MARKETPLACE COMING SOON

Single, shared storefront for services, tools, IP, opportunities, resources, & teaming.