MMEC™

ACCELERATE : COLLABORATE : EXECUTE

PRODUCT OVERVIEW:
DESIGN PLATFORM

design.mmeconsortium.org

DESIGN PLATFORM

One Platform, Every Tool You Need

The DESIGN Platform gives your team one workspace to take a chip from concept to fabrication. It brings together the industry's leading design tools, access to foundry technologies, proven reference flows, & a secure shared workspace, delivered as a managed cloud service. Built for design teams & government programs that need professional-grade tools & performant workspaces without the cost & delay of building & managing them in-house

PLATFORM CAPABILITIES

Commercial & Secure Offerings

Choose the workspace that fits your needs. The Commercial platform serves startups, universities, & design teams in a managed cloud. The Secure platform delivers the same tools & flows in IL5 workspaces for CUI & US Government programs, with IL6 coming soon.

Unified Multi-Vendor Workspace

Run Ansys, Cadence, Keysight, Mentor Graphics, Siemens, Synopsys, & more within a single managed workspace. Combine best-of-breed tools across synthesis, P&R, DRC/LVS, & simulation.

Enterprise License & Instance Management

Administer EDA licenses & cloud compute instances across your whole team from one intuitive dashboard. Allocate seats, monitor utilization, & manage instances without a ticket to IT or a call to a vendor.

Secure Knowledge Management

Integrated MeTRA® knowledge management enables secure file sharing, design data exchange, & team collaboration. Capture, track, & analyze design artifacts across distributed teams with full traceability.

WHY DESIGN PLATFORM?

U.S. GOVERNMENT-READY

IL5 Available Now IL6 COMING SOON

Secure IL5 workspaces for businesses that collaborate with the U.S. Government. Approved for CUI handling & ideal for teams with CMMC compliance requirements.

Start Designing Today

Tools, licenses, PDKs, & reference flows supported & pre-configured. Start designing in hours, not weeks.

Collaborate in Real Time

Fully leverage subscribed compute for organic, in situ design collaboration. Ideal for mentorship & live design reviews.

PDK-Validated Flows

Reference designs carried through DRS, LVS, & timing sign-off with the evidence committed alongside the flow. De-risk new & successfully migrate existing products

VALIDATED FLOW ROADMAP

Complete SoC reference flows covering digital, analog, mixed-signal, & chip integration, with tutorials, PDK onboarding, & committed sign-off evidence. Clone a finished die, change the configuration, start your design.

GlobalFoundries GF130G AVAILABLE NOW

130nm CMOS, RISC-V SoC + 8-bit DAC reference die, RTL through streamed GDSII with full DRC, LVS, & timing sign-off reports.

GlobalFoundries GF22FDX FINAL VALIDATION

22nm FD-SOI for low-power, high-performance RF & mixed-signal design.

GlobalFoundries GF12LP FINAL VALIDATION

12nm FinFET for high-performance digital, AI/ML accelerators, & advanced SoCs.

Flows are configuration-driven & port across process nodes. Ask us about SKY130 & other PDKs.

MMEC's FAMILY OF PRODUCTS

DESIGN PLATFORM | CONNECT PLATFORM | SECURE SERVICES | MARKETPLACE

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